FIG. 1 (PRIOR ART)

14

2

10

16

FIG. 2
(PRIOR ART)

28
60
22
6b
8
24

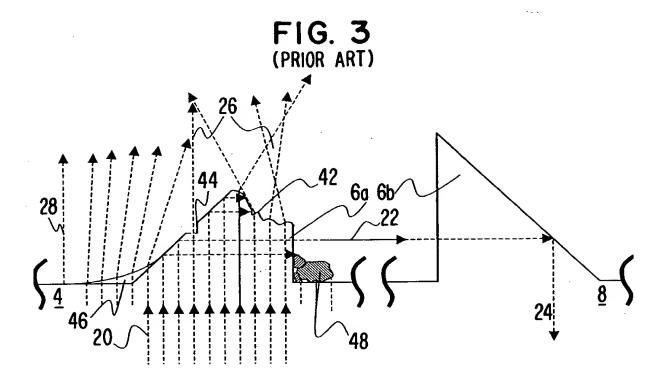


FIG. 4

28

20

29

20

4

20

4

PROCESSOR

PROCESSOR

FIG. 5

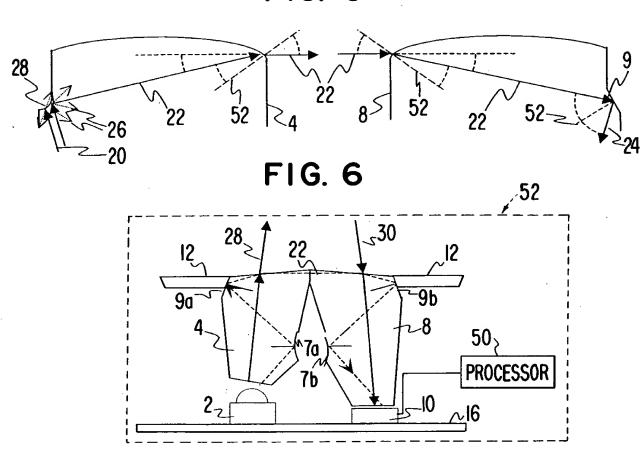


FIG. 7

12

28

22

12

90

4

70

PROCESSOR

22

10

16